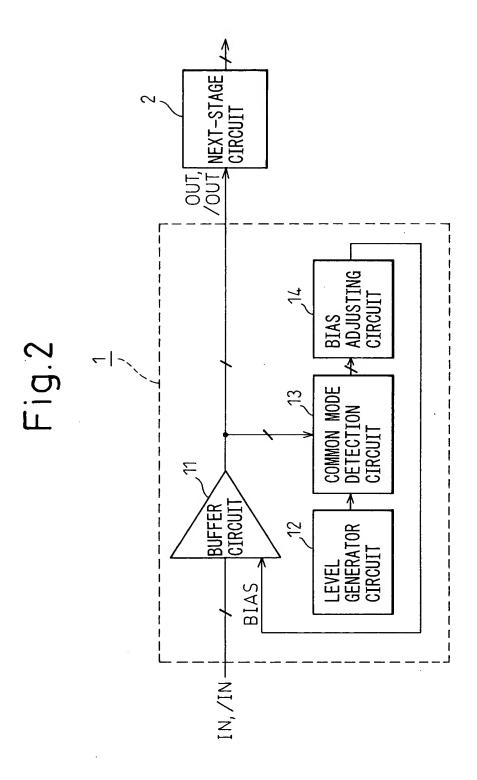
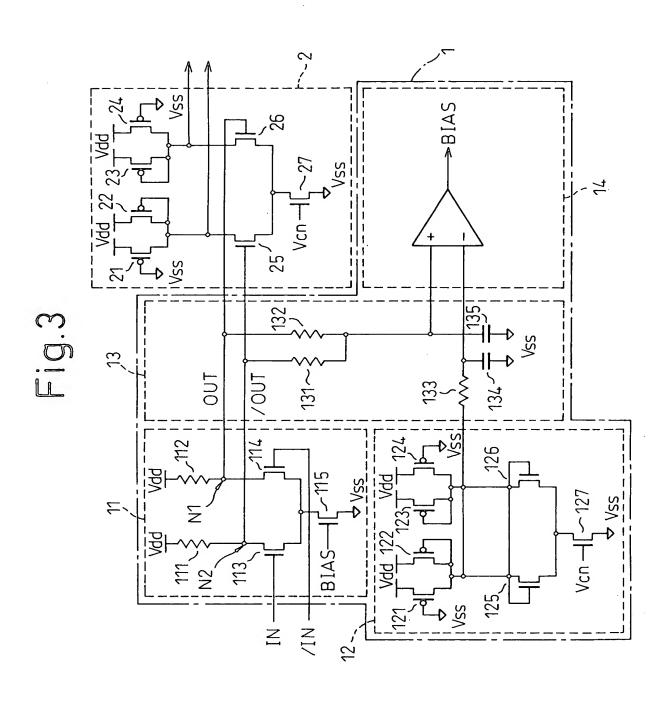
1/8

Fig.1
(PRIOR ART)

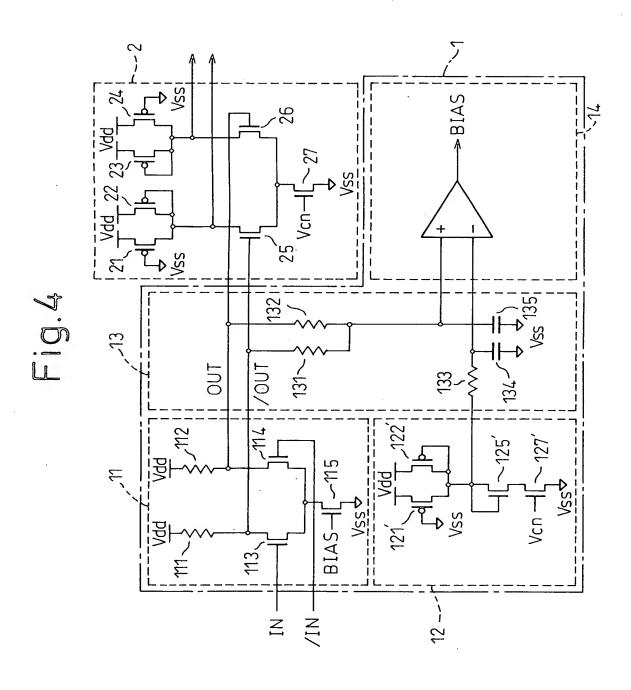
100 Vdd Vdd Vdd 102 /out 103 out 104 bias 105 Vss



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